

FIG. 1

2/9

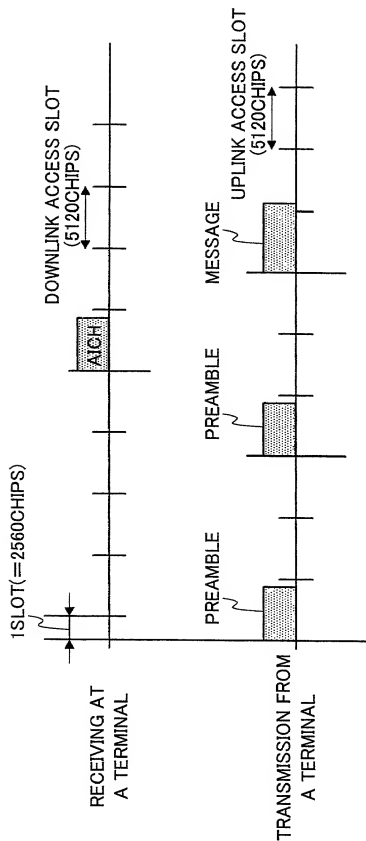


FIG.2

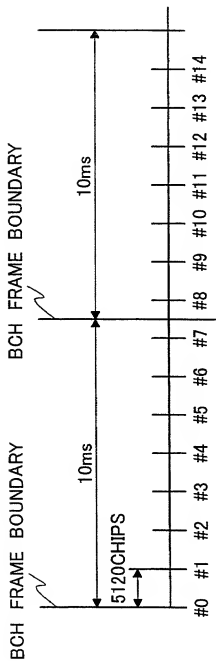


FIG.3

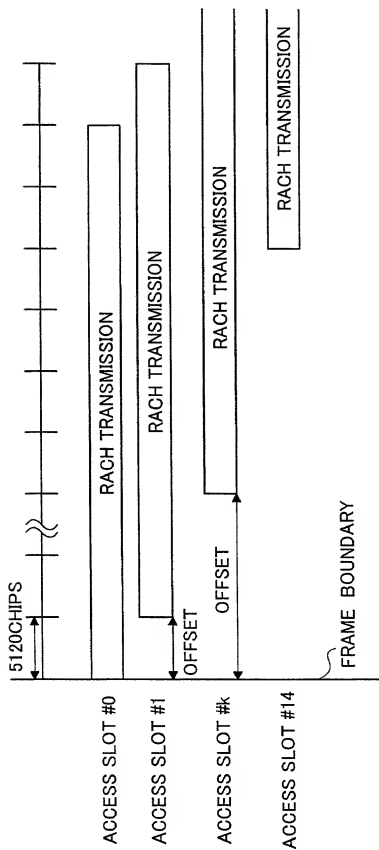
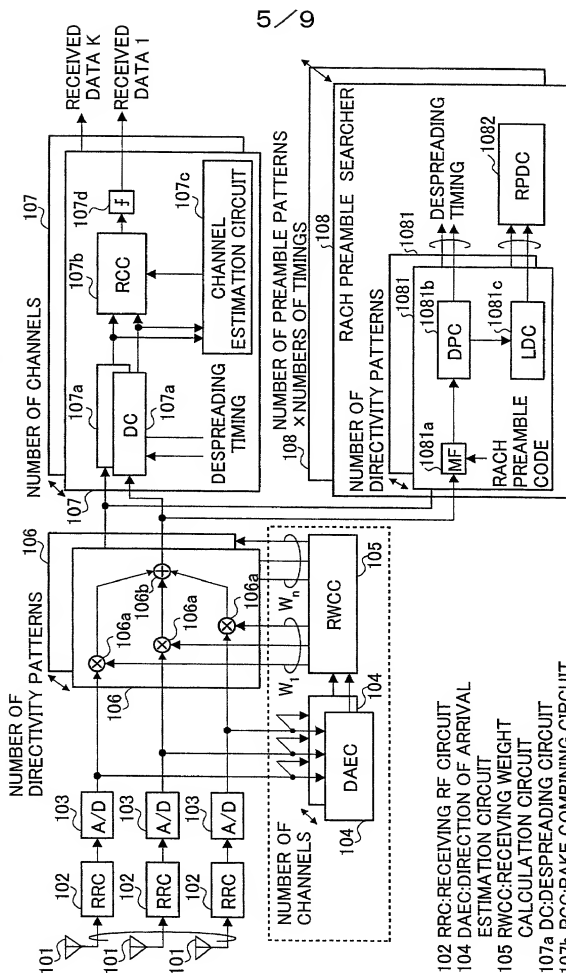


FIG.4



- 102 RRC:RECEIVING RF CIRCUIT
- 104 DAEC:DIRECTION OF ARRIVAL ESTIMATION CIRCUIT
- 105 RWCC:RECEIVING WEIGHT CALCULATION CIRCUIT
- 107a DC:DESPREADING CIRCUIT
- 107b RCC:RAKE COMBINING CIRCUIT
- 1081a DPC:DELAY PROFILE CIRCUIT
- 1081b LDC:LEVEL DETECTION CIRCUIT
- 1082 RPDC:RACH PREAMBLE DETECTION CIRCUIT

6/9

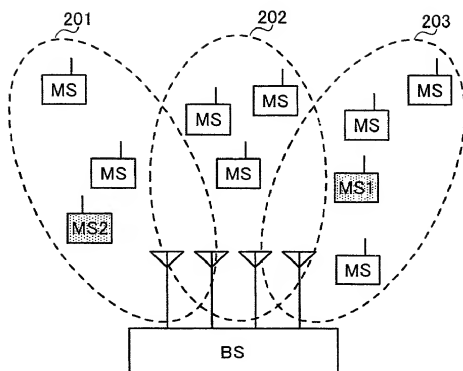


FIG.6

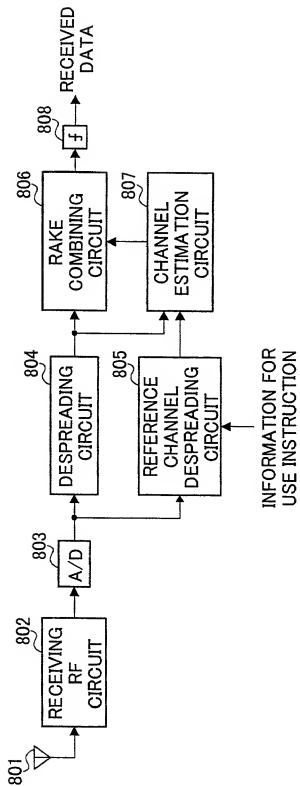


FIG.8

FIG.7

102 RRC: RECEIVING RF CIRCUIT

104 DAEC: DIRECTION OF ARRIVAL ESTIMATION CIRCUIT

105 RWCC: RECEIVING WEIGHT CALCULATION CIRCUIT

107a DC: DESPREADING CIRCUIT

107b RCC: RAKE COMBINING CIRCUIT

107c CEC: CHANNEL ESTIMATION CIRCUIT

108 P × T: NUMBER OF PREAMBLE PATTERNS × NUMBERS OF TIMINGS

1081 NDP: NUMBER OF DIRECTIVITY PATTERNS

1081b DPC: DELAY PROFILE CIRCUIT

1081c LDC: LEVEL DETECTION CIRCUIT

1082 RPDC: RACH PREAMBLE DETECTION CIRCUIT

302 TWCC: TRANSMISSION WEIGHT CALCULATION CIRCUIT

307 TRC: TRANSMISSION RF CIRCUIT

3033 TDPSC: TRANSMISSION DIRECTIVITY PATTERN SELECTION CIRCUIT

3034 TTCC: TRANSMISSION TIMING CONTROLLING CIRCUIT

3035 RCPSC: RACH CODE PATTERN SELECTION CIRCUIT

3031 RLCC: RECEIVING LEVEL COMPARISON CIRCUIT

3032 RDPSC: RECEIVING DIRECTIVITY PATTERN SELECTION CIRCUIT